

SEMICONDUCTOR DEVICE AND METHOD OF PRODUCING THE SAME

BACKGROUND OF THE INVENTION

1. Field of the Invention

5 This invention relates to a semiconductor device and a method of producing the same, and more specifically to a semiconductor device comprising a semiconductor film having a planar surface and a method of producing such a semiconductor device.

10 2. Description of Related Art

A method of producing a thin film transistor (hereinafter referred to as a "TFT") will be described.

Fig. 1 depicts the condition of the surface of a polycrystallized silicon film. Figs. 2A to 2D respectively
15 depict cross sectional views taken along line A-A of Fig. 1 for showing production processes of a conventional TFT.

Step 1 (Fig. 2A):

On an insulating substrate 10 composed of a glass, a silica glass, or the like, an insulating film 11 composed of a
20 single SiO_2 film or of an SiN film and an SiO_2 film is formed, and an amorphous silicon film (hereinafter referred to as an "a-si film") 12 is further formed thereon using a CVD method.

Step 2 (Fig. 2B):

The a-Si film 12 is annealed by irradiating the a-Si film
25 12 with linear excimer laser 14 such as XeCl , KrF , and ArF by scanning from one direction to another, so that the a-Si film 12 is fused and recrystallized for polycrystallization, to

thereby form a polysilicon film (hereinafter referred to as a "p-Si film") 13.

At this time, irradiation of a surface of the a-Si film 12 with an excimer laser beam 14 in the direction shown by an arrow while scanning the a-Si film 12 causes the a-Si film 12 to be fused for recrystallization. Namely, the a-Si film 12, which is heated and fused by the laser radiation 14, is then cooled and recrystallized, thereby forming a p-Si film 13. At this time, however, grain boundaries in the crystal collide with each other causing these areas protrude as shown by projections 100.

Step 3 (Fig. 2C):

A gate insulating film 14 composed of an SiO_2 film is then formed over the entire surface of the p-Si film 13 using the CVD method. A metal film made of a refractory metal such as chromium (Cr) and molybdenum (Mo) is further formed thereon by a sputtering method, and is processed into a predetermined shape by means of photolithography and a dry etching technology using RIE (Reactive Ion Etching), to thereby form gate electrodes 15.

Subsequently, for forming a P channel TFT, P type ions such as boron (B) are implanted (doped) with regard to the p-Si film 13 via the gate insulating film 14, with the gate electrodes 15 used as a mask. When an N type TFT is to be formed, on the other hand, N type ions such as phosphorous (P) are implanted in a similar manner. Thus, a portion of the p-Si film 13, which is an active layer, covered by the gate

electrode 15 functions as a channel region 13c, and portions on both sides of the channel region 13c are a source region 13s and a drain region 13d, respectively.

An interlayer insulating film 16 formed from a single SiO₂ film or from two layers including SiO₂ and SiN films is further disposed using the CVD method.

Step 4 (Fig. 2D):

First contact holes 17 are then formed at locations corresponding to the drain region 13d and the source region 13s, respectively, such that they run through the interlayer insulating film 16 and the gate insulating film 14 to reach the p-Si film 13. Then, drain and source electrodes 19d, 19s, each composed of a multi-metal layer including aluminum and titanium in this order, are formed at the respective first contact holes 17 by patterning the deposited aluminum and titanium, by sputtering, on the interlayer insulating film 16 through which each of the first contact holes 17 is formed, and which fills the first contact hole 17, for example.

Over the interlayer insulating film 16 on which the drain electrode 19d and the source electrode 19s are thus formed and over the drain and source electrodes 19d and 19s, a planarizing insulating film 20 is further disposed for surface planarization. The planarizing insulating film 20 is formed of an acrylic resin layer, which in turn is formed by applying and baking an acrylic resin solution. This acrylic resin layer can smooth the unevenness caused by the gate electrode 15 and the drain and source electrodes 19d and 19s, and

planarize the surface.

Over the source electrode 19s, a second contact hole 21 is formed which passes through the acrylic resin layer, which is a planarizing insulating film 20. A pixel electrode 22 is then formed at the portion of this second contact hole 21 such that this electrode connects to the source electrode 19ss and extends over the acrylic resin layer. The pixel electrode 22 is formed in the following manner. A transparent conductive film, which is an ITO (Indium Tin Oxide) for example, is first disposed on the planarizing insulating film 20 having the second contact hole 21 formed therethrough. After a resist film is applied over the transparent conductive film in a predetermined electrode pattern, the exposed portion of the transparent conductive film is etched by a dry etching method such as the RIE method using HBr gas and Cl₂ as etching gas, to thereby form the pixel electrode 22.

TFTs produced in the above-described manner suffer from problems. As already described, when the a-Si film is fused and recrystallized by laser beam irradiation, the grain boundaries of the crystal collide with each other causing the surface of the p-Si film 13 to have projections 100. Therefore, the gate insulating film 14 formed above these projections has a smaller thickness at these projections 100. These projections can be as thick as the p-Si film. When the thickness of the p-Si film 13 is approximately 400 Å, for example, the projection may be as thick as 400 Å. This leads to a disadvantage that sufficient insulation may not be

achieved between the p-Si film 13 and the gate electrode 15, or that, when the height of projection 100 is larger than the thickness of the gate insulating film 14, there is a short circuit between the p-Si film 13 and the gate electrode 15.

5 Further, concentration of electric field in the projection 100 due to applied voltages causes dielectric breakdown, which also results in possibility of short circuit between the p-Si film 13 and the gate electrode 15.

10 In addition, even when a uniform gate voltage is applied to the gate electrodes 15, a voltage which is actually applied to the p-Si film 13 varies within a surface of the insulating substrate. This results in a TFT having non-uniform characteristics. When such a TFT is employed in a display device such as a liquid crystal display, there is a
15 possibility that variations in display characteristics are caused within the display screen.

SUMMARY OF THE INVENTION

The present invention is made so as to solve the
20 foregoing problems of the related art, and it is an object of the present invention to provide a semiconductor device having excellent characteristics and a method of producing such a semiconductor device, in which projections generated on a semiconductor film are eliminated to planarize the film
25 surface.

In accordance with the present invention, a method of producing a semiconductor device comprises the steps of

forming a non-crystal semiconductor film on a substrate;
heating said non-crystal semiconductor film: and eliminating
projections generated by said heating on said non-crystal
semiconductor film using a physical elimination method.

5 Further, in the above method of producing a semiconductor
device, said heating step may comprise a step of applying
laser beam irradiation for fusing and recrystallizing said
non-crystal semiconductor film.

10 Further, in the above methods of producing a
semiconductor device, said physical elimination method may
comprise a method of applying ion beam irradiation to said
projections to eliminate said projections.

15 Still further, in the above methods of producing a
semiconductor device, an angle θ formed by the incident
direction of the ion beam from said ion milling and the
direction of the normal line of (the perpendicular with
respect to) a surface of said non-crystal semiconductor film
is 60° to 90° .

20 Also, in accordance with another aspect of the present
invention, a semiconductor device comprises a substrate and a
non-crystal semiconductor film formed on said substrate,
wherein said non-crystal semiconductor film has a planar
surface by eliminating, using ion beam irradiation,
projections generated on said non-crystal semiconductor film
25 due to heating of said film.

According to the present invention, projections generated
on the surface of p-Si (non-crystal semiconductor) film can be

effectively eliminated such that the surface can be planarized using the ion milling method, allowing a semiconductor device having excellent characteristics can be obtained.

5 BRIEF DESCRIPTION OF THE DRAWINGS

These and other objects of the invention will be explained in the description below, in connection with the accompanying drawings, in which:

FIG. 1 shows a surface condition of a semiconductor
10 device according to a related art;

FIGS. 2A, 2B, 2C and 2D are cross sectional views for explaining processes of a production method of a semiconductor device according to a related art;

FIGS. 3A, 3B, 3C, 3D and 3E are cross sectional views for
15 explaining processes of a production method of a semiconductor device according to the present invention;

FIG. 4 is a cross sectional view of a liquid crystal display in which a semiconductor device of the present invention is employed;

20 FIG. 5 is a cross sectional view of an ion milling device for use in the method of producing a semiconductor device according to the present invention;

FIGS. 6A, 6B, and 6C are cross sectional views for explaining an etching process of the method of producing a
25 semiconductor device according to the present invention;

FIG. 7 is a characteristics view showing a relationship between the incident angle of ion beams and the etching rate

according to the present invention; and

FIG. 8 is a view showing a relationship between the incident angle of ion beams and the height of a projection after planarization according to the present invention.

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DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENT

A preferred embodiment of the present invention will be described in further detail with reference to the accompanying drawings.

10 An example will be described where a method of producing a semiconductor device according to the present invention is adopted for a liquid crystal display comprising TFTs.

15 Figs. 3A to 3E are cross sectional views for explaining the producing processes of a TFT according to the present invention, and Fig. 4 is a cross sectional view of a liquid crystal display.

Step 1 (Fig. 3A):

On an insulating substrate 10 composed of a glass, a quartz glass or the like, an insulating film 11 composed of a single SiO_2 film or of an SiN film and an SiO_2 film is formed using the CVD method or the like, for the purpose of preventing impurities including sodium ions in the insulating substrate from entering a semiconductor film (p-Si film) formed thereon. When a nonalkaline glass substrate or the like which can be guaranteed to remain free of impurities is employed, formation of the insulating film 11 is not essential.

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In the present invention, the insulating substrate includes a substrate having insulating properties in a surface. Namely, the insulating substrate may be formed from a semiconductor substrate having an insulating film 11

5 composed of an SiN film and an SiO₂ film deposited thereon.

An a-Si film 12 is then formed on the insulating film 11 using the CVD method. The thickness of the a-Si film 12 may be 300Å to 1000Å, but is set at 400Å in example of the present embodiment.

10 Step 2 (Fig. 3B):

The a-Si film 12 is annealed by irradiating the a-Si film 12 with excimer laser formed in a linear shape having wavelength of 308nm from one direction to the other for scanning, so that the a-Si film 12 is fused and recrystallized for polycrystallization, to thereby form a polysilicon film (hereinafter referred to as a "p-Si film") 13.

In this step, irradiation of a surface of the a-Si film 12 with an excimer laser light 14 causes the a-Si film 12 to be fused for recrystallization. Namely, the a-Si film 12, which is heated and fused by the laser irradiation, is then cooled and recrystallized, thereby forming a p-Si film. At this time, grain boundaries in the crystal collide causing portions to protrude as shown by projections 100.

As example lasers, an XeCl excimer laser having a wavelength λ of 308nm or an ArF excimer laser having a wavelength λ of 193nm may be used.

Step 3 (Fig. 3C):

According to this embodiment, the projections 100 are etched by irradiation using ion beams 110 applied from an ion milling apparatus.

More specifically, Ar ion irradiation 110 is applied to
5 the p-Si film 13 in a direction which forms an angle θ with respect to the surface of the p-Si film 13.

Step 4 (Fig. 3D):

In this manner, the projections 100 on the surface of the p-Si film 13 are eliminated to thereby planarize the surface
10 of the p-Si film 13.

Step 5 (Fig. 3E):

A gate insulating film 14 formed from an SiO_2 film is then disposed over the entire surface of the p-Si film 13 using the CVD method. A metal film made of a refractory metal
15 such as Cr or Mo is further formed by a sputtering method, and is processed into a predetermined shape by means of photolithography and a dry etching technology using RIE, to thereby form gate electrodes 15.

Subsequently, using the gate electrodes 15 as a mask, P
20 type or N type ions are implanted into the p-Si film 13 via the gate insulating film 14. Namely, in accordance with the type of TFT to be formed, P or N type ions are implanted into the regions of the p-Si film 13 not covered by the gate electrodes 15.

25 More specifically, when forming a P channel TFT, P type ions such as boron (B) are implanted, whereas when forming an N type TFT, N type ions such as phosphorous (P) are implanted.

Thus, a portion of the p-Si film 13, which is an active layer, covered by the gate electrode 15 functions as a channel region 13c, and portions on both sides of the channel region 13c are a source region 13s and a drain region 13d, respectively.

5 Then, an interlayer insulating film 16 composed of a single SiO₂ film or of two layers including SiO₂ and SiN films is further formed using the CVD method.

First contact holes 17 are then formed at locations corresponding to the drain and source regions 13d and 13s, respectively, such that they run through the interlayer insulating film 16 and the gate insulating film 14 to reach the p-Si film 13. Then, drain and source electrodes 19d and 19s, each composed of a multi-metal layer including aluminum and titanium in this order, are formed at the respective first contact holes 17 by patterning the deposited aluminum and titanium, by sputtering, on the interlayer insulating film 16 through which each of the first contact holes 17 is formed. The aluminum also fills the first contact hole 17, for example.

20 Over the interlayer insulating film 16 on which the drain and source electrodes 19d and 19s are thus formed, and on the drain and source electrodes 19d and 19s, a planarizing insulating film 20 is further disposed for surface planarization. The planarizing insulating film 20 is formed from an acrylic resin layer, which in turn is formed by applying and baking an acrylic resin solution. This acrylic resin layer can smooth the unevenness caused by the gate

electrode 15 and the drain and source electrodes 19d and 19s to planarize the surface.

Further, over the source electrode 19s, a second contact hole 21 is formed which passes through the acrylic resin layer, being the planarizing insulating film 20. A pixel electrode 22 is then formed at the portion of this second contact hole 21 so that it connects to the source electrode 19s and extends over the acrylic resin layer. The pixel electrode 22 is formed in the following manner. A transparent conductive film, which is an ITO (Indium Tin Oxide) for example, is first disposed on the planarizing insulating film 20 having the second contact hole 21 formed therethrough. After a resist film is applied over the transparent conductive film in a predetermined electrode pattern, the exposed portion of the transparent conductive film is etched by a dry etching method such as the RIE method using HBr gas and Cl₂ as etching gas, to thereby form the pixel electrode 22.

Then, for aligning the liquid crystal, an alignment layer 23 formed from polyimide, SiO₂, or the like is disposed over the pixel electrode 22 and the planarizing insulating film 20 using a printing method or a spinner method .

In this manner, a TFT substrate 10 which is one substrate of a liquid crystal display using a TFT which drives the liquid crystal as a switching element, is complete.

Next, on an opposing electrode substrate 30 which is an insulating substrate formed from a quartz glass or a nonalkaline glass, an opposing electrode 31 formed from a

transparent conductive film such as an ITO film is disposed so as to cover the whole surface of the substrate 30, and an alignment film 32 formed from polyimide, SiO_2 , or the like is formed thereon for aligning the liquid crystal 24.

5 The opposing electrode substrate 30 is thus disposed so as to face the above-referenced TFT substrate 10. The substrates 10, 30 are then adhered to each other using a sealing agent made of a resin having adhesive property, and which is provided between and in the vicinity of the TFT substrate 10 and the opposing electrode substrate 30. The liquid crystal 24 is then injected between these substrates 10, 30 to complete the liquid crystal display as shown in Fig. 4.

10 Now, the principle of an ion milling apparatus for eliminating the projections 100 generated on the surface of the p-Si film 13 will be described.

15 Fig. 5 is a cross sectional view schematically showing an ion milling apparatus.

Referring to Fig. 5, the ion milling apparatus comprises an ion source region IS for generating ions and an etching chamber region EC for applying ion irradiation to an irradiation object to etch the object. Both regions are evacuated, with the degree of vacuum being 1×10^{-6} Torr (1 Torr \doteq 133Pa).

20 The ion source region IS comprises a gas supply port 210 for supplying a gas, such as argon gas, which is ionized by a magnet 230; a cylindrical anode 231 surrounded by the magnet

230 for generating a magnetic field to cause a plasma gas; a cathode 240 composed of a filament for emitting a thermion; and an extraction electrode 250 for extracting Ar ions from the generated plasma.

5 The etching chamber region EC, on the other hand, comprises a neutralizer 260 for emitting electrons to neutralize Ar ions extracted by the extraction electrode 250;, and a stage 290 for fixing the etching object. The chamber also includes an exhaust port 300 which uses a pump (not
10 shown) for exhausting gases from the chamber.

A glass substrate 270, on which a p-Si film 280, the etching object, is formed to cover the whole surface, is fixed on the stage 290. The stage 290 is rotated at a predetermined rate, so that the p-Si film 280 disposed on the glass
15 substrate 270 is uniformly irradiated with Ar atoms, as shown by arrows 110. At this time, the Ar atom irradiation applied from the ion source region IS enters the surface of the stage 270 at an angle which is inclined by θ with respect to the normal direction of the surface of the substrate 270. Namely,
20 the Ar atoms enter the p-Si film 280 from the direction forming an angle of $((\pi/2)-\theta)$ with regard to the surface of the p-Si film 280. In this manner, the Ar atoms are applied at a predetermined angle θ with regard to the projections 100 generated due to the polycrystallization annealing on the p-Si
25 film 280, thereby etching the projections 100. The angle θ may be optionally changed by adjusting the fixing angle of the stage 290.

In the above-described ion milling apparatus, the interior of the ion source region IS and the etching chamber region EC is evacuated using a diffusion pump or the like. Ar gases are then supplied via the gas supply port 210 into the ion source region IS while a voltage is applied to the anode electrode 231, the magnet 230, and the cathode 240, to thereby turn the Ar gases into a plasma condition. A voltage of approximately 800 V is applied to the extraction electrode 250 for extracting Ar ions from the Ar plasma into the etching chamber region EC. Electrons are then supplied from the neutralizer 260 and applied to the thus extracted Ar ions so that the Ar ions are combined with the electrons, thereby forming Ar atoms. The Ar atoms are then caused to collide with the p-Si film 280 on the glass substrate 270 which is fixed on the stage 290. The Ar atoms collide with the projections 100 generated on the surface of the p-Si film 280, thereby eliminating the projections 100.

The etching of the projections 100 using Ar atoms will next be described.

Figs. 6A to 6C show how etching is performed with regard to projections having various shapes using Ar atom irradiation.

Specifically, Figs. 6A, 6B and 6C respectively show etching for a conical projection, a plurality of conical projections disposed in series, and a projection having a rectangular cross section.

Referring first to Fig. 6A, etching of a conical

projection will be described.

In this example, a projection 100 has an elevation angle α with regard to the surface of the p-Si film 280, and Ar atoms enter the surface of the p-Si film 280 from a direction which is inclined by an angle θ with regard to the perpendicular (normal line) VL1 of the film surface.

In this case, Ar atoms enter the slope of the conical projection 100 from a direction inclined by an angle $(\theta - \alpha)$ with regard to the perpendicular VL2 of the slope. Because the slope of the projection 100, although inclined, forms a flat plane on the surface of the p-Si film, the Ar atoms enter this flat surface.

At this point, the glass substrate 10 on which the p-Si film 280 is formed is fixed on the stage 290 and therefore rotates with the stage 290, so that the whole surface of the p-Si film 280 is uniformly irradiated with the Ar atoms. Namely, the Ar atoms are sequentially applied to the slope of the projection and the planar portions of the p-Si film 280. However, as already described, the incident angle $\theta - \alpha$ of the Ar atoms with regard to the slope of the projection 100 differs from the incident angle θ of the Ar atoms with regard to other planar portions. Because the etching rate of the ion milling which will be described depends on the beam incident angle, the projection 100a is etched at a higher rate than the planar portions of the p-Si film other than the projection. Consequently, the projection 100a is sequentially etched into a smaller projection 100b, 100c, to be finally eliminated

completely. As a result, a p-Si film 13 having a planar surface can be obtained.

Referring next to Fig. 6B, the milling effect for a plurality of conical projections formed in series will be described.

As in the example the projection shown in Fig. 6A, this example again describes a projection 100 having an elevation angle α with regard to the surface of the p-Si film 280, and Ar atoms entering the surface of the p-Si film 280 from a direction inclined by an angle θ with regard to the normal line VL1 of the p-Si film. In this case, Ar atoms enter the slope of each conical projection 100 from a direction inclined by an angle $(\theta - \alpha)$ with regard to the normal line VL2 of the slope. Consequently, each projection 100 is sequentially etched into a smaller projection as shown by 100a, 100b, and 100c, so that the surface of the p-Si film 280 can be planarized.

Next, referring to Fig. 6C, the milling effect for a cylindrical projection will be described.

In this example, a projection 100 projects vertically with regard to the planar surface of the p-Si film 280, and Ar atoms 110 enter the p-Si film 128 from a direction inclined by an angle θ with regard to the normal line VL1 of the planar surface of the p-Si film 280.

In this case, the surface direction of the top surface of the projection 100 corresponds with that of the planar surface of the p-Si film 280. On the other hand, Ar atoms enter the

side surface of the projection 100 from a direction inclined by an angle $((\pi/2)-\theta)$ with regard to the normal line VL2 of the side surface VS. Here, the side surface VS also forms a planar surface.

5 Therefore, the incident angle of the Ar atoms with regard to the side surface VS of the projection 100 differs from the incident angle of the Ar atoms with regard to the planar surface of the p-Si film 280 and the top surface of the projection 100. When the p-Si film 280 is sequentially
10 irradiated with the Ar atoms, the side surface VS of the projection 100 is selectively etched rather than the top surface thereof. Consequently, the projection 100a is sequentially etched into a smaller projection 100b, 100c, to be finally eliminated completely. As a result, a p-Si film 13
15 having a planar surface can be obtained.

The relationship between the irradiation angle of Ar atoms and the etching rate of the p-Si film when the p-Si film is irradiated with Ar atoms will next be described.

Fig. 7 depicts a relationship between the irradiation
20 angle of Ar atoms and the etching rate of the p-Si film when the p-Si film having a planar surface is irradiated with Ar atoms. In Fig. 7, the horizontal axis depicts the irradiation angle of Ar atoms with regard to the normal direction of the p-Si film surface whereas the vertical direction depicts the
25 etching rate at which the p-Si film is etched by the Ar atoms.

As shown in Fig. 7, the etching rate for etching silicon varies according to the incident direction of Ar ions (Ar ion

beams). Fig. 7 shows a case where the beam energy of Ar atoms is 500eV and the current density of Ar atoms is 1.4 mA/cm².

The etching rate gradually increases as the incident angle θ of Ar atoms increases from 0°, to reach the maximum when θ is 60°. The etching rate then decreases sharply in the vicinity of the angle θ being 60° to 90°.

The etching of a conical projection shown in Fig. 6A will be described again. In order to eliminate a projection by irradiation of ion beams, it is preferable that the etching rate for the projection be high while the etching rate for the planar surface of the film be low. Namely, in the case of conical projection shown in Fig. 6A, it is also preferable that the projection 100a is etched quickly while the planar surface of the film is difficult to etch.

An example case wherein the incident angle θ of the ion beams is 88° and the angle α of the conical projection with regard to the surface of the p-Si film 13 is 60° will be described in the following.

More specifically, in Fig. 6A, the beam incident angle is the angle θ , being 88° with respect to the perpendicular VL1, and ion beams enter the planar portion of the p-Si film 280 at this incident angle. On the other hand, the beam incident angle is $(\theta - \alpha)$, or 28° (= 88° - 66°) with respect to the normal line VL2 of the slope of the projection 100. Referring again to Fig. 7, when the planar surface of the p-Si film is irradiated at the incident angle of 88°, the etching rate is approximately 100 Å/min whereas when the incident angle is

28°, the etching rate is approximately 600 Å/min. Namely, the projection is etched about six times faster than the planar portion of the p-Si film, so that etching of the projection can be completed without etching a significant portion of the planar surface.

For the projections having various shapes as shown in Figs. 6A to 6C, it is also possible to effectively etch the projections without etching the planar portions of the film, by appropriately selecting the incident angle of the ion beams such that the etching rate for the projection is higher than the that for the planar portions.

Fig. 8 depicts an example of a projection formed on a p-Si film after the p-Si film is irradiated with Ar atoms at different incident angles. In Fig. 8, the horizontal axis shows angle with respect to the normal line of the substrate surface, and the vertical axis shows the average height of a projection on the p-Si film after the Ar atom irradiation. In this example, the average height of the projection is 400Å and the projection has a conical shape as shown in Fig. 8.

As shown in Fig. 8, as the incident angle increases, the projection becomes lower and is eliminated, so that the surface of the p-Si film is planarized.

It is desirable that the projection of the p-Si film which is an active layer not be high because, when the projection penetrates through the insulating film formed thereon, not only can the insulation property not be obtained, but the active layer may also short-circuit the conductive

layer formed on the insulating film. It is therefore preferable for the remaining projecting p-Si film to have a thickness capable of substantially maintaining the insulation property.

5 Consequently, because a projection of 250 Å is tolerable, the incident angle may be 60°. Further, as a projection of 200 Å is more preferable, the incident angle may be 70°. Still further, because a remaining projection of 150 Å is still more preferable, the incident angle may be 80°.

10 As described above, sufficient insulation can be achieved between the p-Si film 13 and the gate electrodes 15, by applying ion beam irradiation onto the projections generated on the surface of the p-Si film using the ion milling method to planarize the surface. Further, even when the height of the projection 100 is larger than the thickness of the gate insulating film 14, a short circuit between the p-Si film 13 and the gate electrodes 15 can be prevented by eliminating the projections 100 to thereby planarize the surface by ion
20 milling.

The elimination of the projections 100 can also solve the problem of concentration of electrical field in the projection when a voltage is applied to the p-Si film.

25 Further, the problem with the related art that, even when a uniform gate voltage is applied to the gate electrodes 15, a voltage which is actually applied to the p-Si films 13 varies within a surface of the insulating substrate such that the

characteristics of a TFT become non-uniform, can be reduced. As a result, when a TFT according to the present invention is employed in a display such as liquid crystal display or the like, variations of display characteristics within the display
5 screen can be eliminated.

In the foregoing embodiment, a glass substrate 270 forming a single liquid crystal display panel is fixed on the stage 290. However, the glass substrate 270 may be a so-called mother glass for forming a plurality of liquid crystal
10 display panels, which can provide similar effects. The glass substrate 270 may also be formed from a glass having a low melting point, because, with the present invention, a-Si can be polycrystallized at a low temperature using laser annealing.

15 While the preferred embodiment of the present invention has been described using specific examples and terms, such description is for illustrative purposes only, and it is to be understood that changes and variations may be made without departing from the spirit or scope of the appended claims.